



## **A 2-day System Verification and Validation Workshop**

### **Workshop Description**

The 2-day System Verification and Validation Workshop is complementary to and a logical follow-on to the System Design Workshop. We recommend that the System Design Workshop be attended before the System Verification and Validation Workshop.

The System Verification and Validation Workshop teaches participants about the systems approach to the Verification and Validation (V&V) of complex systems. The workshop emphasizes the need to undertake V&V early through Design V&V as well as the more traditional Build V&V to create a body of evidence that demonstrates the maturity of a system design. The workshop introduces a number of simple but extremely powerful tools.

The taught element of the workshop is supported by a unifying case study where Participants working in small teams can practice the processes and tool set.

### **Course Numbers and Who Should Attend?**

The 2-day System Verification and Validation Workshop can be delivered to up to 20 participants. The workshop applies equally to the V&V of product-based as to service or process-based systems. The workshop is therefore suitable for all personnel involved in the introduction and through life support of any complex system.

### **Benefits to the Individual and Business**

During an intensive two days of teaching and practical 'hands on' exercises, participants will be challenged to develop the skills and mind-set that can be applied to any system design irrespective of type, scale or context.

At the end of the workshop participants will:

- have an understanding the principles of a systems approach to verification and validation
- know the differences between validation and verification and understand the various methods, and their relative merits for accomplishing verification and validation
- understand and explain the verification and validation opportunities throughout the system development cycle
- understand the principles and concepts of a systems approach to system verification and validation through system characterisation as opposed to system demonstration testing
- understand and explain the concept of robust optimisation and system sensitivity
- clearly define the differences between determination and demonstration testing and how they relate to the system approach to verification and validation
- understand the principles of designed experiments to explore the design solution space
- for robust optimums
- clearly describe the fundamental process, activities and methodology for a systems approach to system verification and validation
- be able to write Verification and Validation requirements.

### **Learning Approach**

The learning approach is based on the Kolb learning cycle with a significant proportion of the course set aside for exercises to reinforce the learning. Indeed, many of the small group exercises involve a case study that provides a practical focus for the course and enables the Participants to practise the methodology and tools presented.

## Workshop Structure and Content

Day 1	Day 2
<p><b>Introduction and Delegate expectations</b> Review of Systems Approach to Systems Design</p> <ul style="list-style-type: none"> <li>• Designing in levels and the V diagram</li> <li>• Generic system design process</li> </ul> <p>A Systems Approach to Verification and Validation</p> <ul style="list-style-type: none"> <li>• Build V&amp;V and Design V&amp;V</li> </ul> <p>Build V&amp;V</p> <ul style="list-style-type: none"> <li>• V&amp;V methods</li> <li>• V&amp;V Requirements</li> <li>• V&amp;V Planning</li> <li>• Verification Compliance Matrix and maturing V&amp;V evidence</li> </ul> <p>Design V&amp;V</p> <ul style="list-style-type: none"> <li>• Sensitivity analysis</li> <li>• Failure analysis using Functional and Design FMEA</li> </ul>	<p><b>Review of Day 1</b></p> <ul style="list-style-type: none"> <li>• Verifying and Validating Requirements and System design using Quality Function Deployment</li> </ul> <p>Design Verification through Robust Design</p> <ul style="list-style-type: none"> <li>• Taguchi, Noise and Loss functions</li> <li>• Parameter and Tolerance design</li> <li>• Design of experiments to characterise the design</li> <li>• Full factorial and fractional factorial design of experiments</li> </ul> <p>Summary</p>

## Workshop Costs

The cost of delivering the 2-day workshop, excluding delivery tutor accommodation and expenses, but including all courseware is £3,000. VAT will apply at the prevailing rate.

The workshop can be tailored to suit individual customer's operations.

## More Information and Contact Details

For more information about the 2-day System Verification and Validation Workshop or any of our other Systems Engineering courses please contact **Dr Stuart Burge** on +44 (0) 7803 131614 or [sburge@burgehugheswalsh.co.uk](mailto:sburge@burgehugheswalsh.co.uk).